

Validated and Protected IP Worldwide



SoC designers in the embedded market are faced with the challenge of building differentiated products in a timely fashion and at highly competitive consumer price points. Paramount to a design's success is an established and robust ecosystem of tools and software available to support a design from start to finish. Also increasingly critical in SoC design is the use and reuse of verified intellectual property. Without a robust design environment or quality intellectual property, SoC designers and OEMs run the risk of missing critical budget targets and market introductions—potentially sentencing a design to failure.

The MIPS-Verified™ brand helps designers clearly distinguish high-quality, dependable products and well-supported interoperable solutions that are exclusively compatible with the MIPS® Ecosystem. Core or architecture implementations marked by the MIPS-Verified brand ensure that the intellectual property has been rigorously tested—leveraging years of design verification technology.

The design verification technology is regularly refined and enhanced by a team of engineers to test every new release and every new feature added to the MIPS® architecture. It comprises hundreds of tests for validating individual functions of a design, not only with architectural compliance but also in authenticating a product throughout the design cycle.

Ensuring the Integrity of MIPS-Based™ Products

Core and architecture licensees are provided with the necessary tests appropriate to their licensed technology and can easily self-certify once tests are completed. Since all cores from MIPS Technologies are pre-verified before release, licensees are re-verifying that their core remains compliant. Undisclosed mechanisms in the submission process ensure that all design verification is rigorous and complete.

About the MIPS® Ecosystem

With millions invested in it, the MIPS Ecosystem offers the silicon design community one of the richest and most comprehensive development environments available in the embedded space. Between hundreds of third-party tools and software, platform solutions, development systems, and system IP, SoC designers and OEMs working on MIPS-Based™ applications are able to quickly deliver compelling and competitive solutions. Today, MIPS-Based designs are integrated in millions of products around the world, including broadband devices from Linksys, digital cameras from Canon, DTVs and entertainment systems from Sony, DVD Recordable devices from Pioneer, digital set-top boxes from Motorola, network routers from Cisco and laser printers from Hewlett-Packard.



At the core of the user experience.®

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