Company

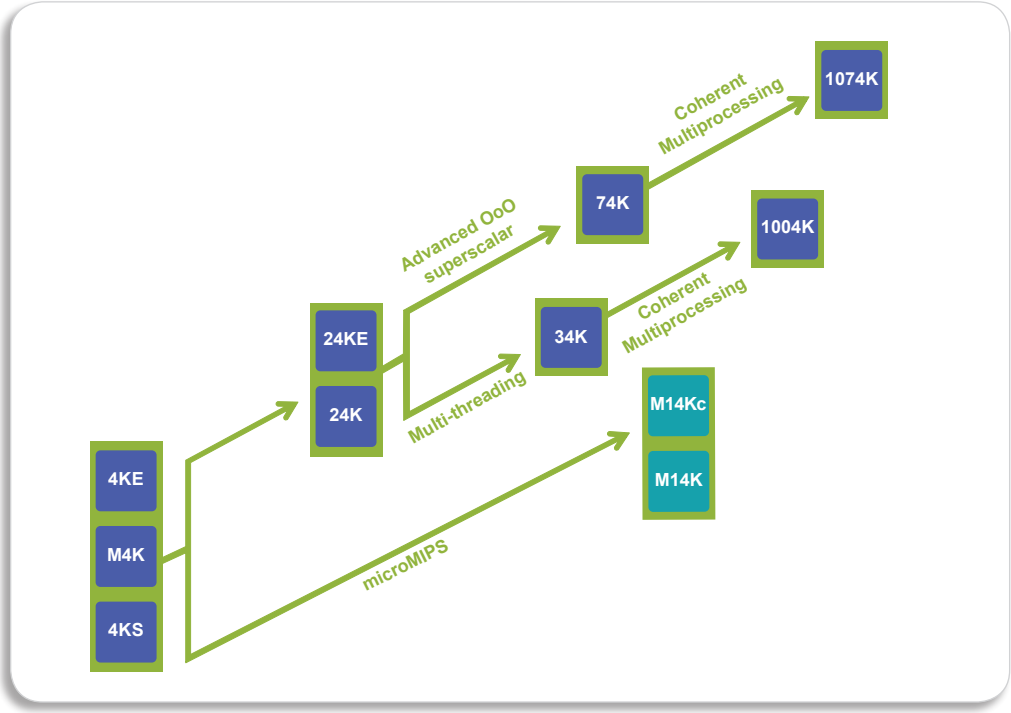
MIPS Technologies is a leading provider of industry-standard processor architectures and cores for digital home, networking and mobile applications. MIPS-Based™ designs are integrated into tens of millions of the world's most popular products, including broadband devices from Linksys, DTVs and digital consumer devices from Sony, DVD recordable devices from Pioneer, digital set-top boxes from Motorola, network routers from Cisco, 32-bit microcontrollers from Microchip Technology and laser printers from Hewlett-Packard. MIPS licenses its award-winning technologies to leading semiconductor companies, ASIC developers and system OEMs. The company's licensees have together shipped more than three billion MIPS-Based products.

Products

MIPS Technologies offers a comprehensive processor product line, delivering solutions for everything from low-power microcontrollers and mobile devices to feature-rich set- top boxes and HDTVs—with unique efficiencies and cost advantages for next-generation SoC design. Backed by its own development tools and a vast and robust ecosystem of partners dedicated to best-in-class development support, MIPS Technologies helps customers successfully design advanced solutions on time with less risk and lower cost.

- Open, licensable 32-bit and 64-bit architectures for maximum design flexibility with seamless 32-/64-bit compatibility to protect software investment
- Delivering industry-leading single-threaded, multi-threaded and multicore performance
- Broad range of synthesizable and configurable 32-bit processor cores that enable high performance, low power consumption and small silicon die area for tomorrow's converged devices
- Enabling a rich internet-connected experience with Android, HTML5 web technologies, Adobe Flash Player, Skype and other key platforms
- Rapid, reliable, cost-effective development, supported by hundreds of tools and the most popular operating systems, including Linux, VxWorks, Windows CE, Nucleus and ThreadX
- Hardware verification and advanced system debug technologies

MIPS® 32-bit Cores



Markets

MIPS Technologies has the leading processor architecture across a wide array of high-growth markets, including digital television, set-top boxes, Blu-ray disc players, Wi-Fi routers and residential gateways.

As a long-time leader in the digital consumer and connected home markets, MIPS Technologies has gained strong traction over the years in video games and consoles, digital cameras, office automation products, Wi-Fi and cable modems and broadband access solutions. Other key markets where MIPS has demonstrated considerable growth include 32-bit microcontrollers, automotive, mobile handsets and other consumer electronic devices.

Ecosystem

The MIPS ecosystem provides the silicon design community with one of the richest and most comprehensive environments offered in today's embedded space. Leveraging a wide range of third-party tools and software, platform solutions, development systems and system IP, SoC designers and OEMs working on MIPS-Based applications can rapidly deliver compelling and competitive solutions.

OCEAN TOMO 300® PATENT INDEX

MIPS TECHNOLOGIES

The Heritage of the MIPS® Architecture

At the heart of MIPS Technologies is the MIPS architecture, developed in the 1980s by Professor John Hennessy, now president of Stanford University. With MIPS, Hennessy created an elegant, streamlined architecture with scalability that has met the demands of generations of applications, preserving the wealth of development tools and software that support them. Today, the MIPS architecture is a widely-used, widely-taught architecture with millions of lines of code written for it, and it is the architecture of choice for home entertainment, home networking and beyond. Innovation on the MIPS architecture continues through MIPS Technologies and our architectural licensees including Broadcom Corporation, Cavium Networks, Ingenic Semiconductor, The Institute of Computing Technologies of the Chinese Academy of Sciences (ICT), Loongson Technology, NEC Electronics, NetLogic Microsystems, Renesas Electronics, Sony, Toshiba Corporation and others.

Driving Next-Generation Connected Devices

Next-generation consumer electronic devices will be increasingly connected to the internet, networked together, and converged into multi-function solutions. MIPS Technologies has the right architecture, cores and ecosystem to meet the growing performance, price, and power challenges of tomorrow's connected devices.

The Linux operating system is key for connected devices, and MIPS Technologies is active in Linux kernel development and working within the Linux community. Today, MIPS Technologies is one of the world's top ten contributors to the Linux kernel. MIPS also provides innovative tools to aid development, debugging and profiling of Linux- based systems. A large number of companies successfully deliver Linux-based systems running on MIPS-Based hardware.

MIPS Technologies is also an industry leader in developing and proliferating new technologies that will create the next generation of connected devices. This includes Android, a virtualized open source platform backed by Google. Initially finding success in mobile phones, Android is now moving beyond that to become a standard way to bring the full internet experience to tablets, DTVs, set-top boxes, and other embedded devices. With Android on MIPS, developers can easily take advantage of a feature-rich, open source, internet-connected platform that lowers software costs and reduces time-to-market.

The Best Platform for the Convergence Era

MIPS Technologies has long been known as the embedded processor leader in digital home products. This leadership position in high-performance processing for multimedia and networking applications, and the company's Android on MIPS initiative, are enabling the company to expand into other high-growth markets such as mobile products. This is helped along by market inflection points such as the global transition to 4G. The high bandwidth requirements of 4G are very similar to wireless technologies such as Wi-Fi, where MIPS Technologies has a strong presence. With the continued integration of high-speed communications and HD quality multimedia into handsets, MIPS' long legacy and experienced design support in these areas can be leveraged for advantage in mobile designs.

MIPS truly is the architecture of choice for the convergence era. With MIPS, customers gain time-to-market by using a proven architecture from a trusted supplier. Customers also benefit from the MIPS ecosystem, which provides the applications and development tools necessary for success. MIPS Technologies offers the best combination of price, performance and area for SoC vendors to meet their design targets.

At the core of the user experience.®

Contact

Headquarters

MIPS Technologies, Inc.
955 East Arques Avenue
Sunnyvale, CA 94085
United States
Phone: 408 530 5000
Fax: 408 530 5150
www.mips.com
info@mips.com

MIPS Technologies, Inc. (Oregon)
Beaverton, Oregon
Phone: 503 597-5091
Fax: 503 924-1110

MIPS Technologies (Shanghai) Co., Ltd.
Shanghai, China
Phone: +86 21 6385 8383
Fax: +86 21 5306 0833

MIPS Technologies B.V.
Jhubei, Taiwan
Phone: +886 3 6583 561
Fax: +886 3 6583 563

MIPS Technologies B.V.
Tokyo, Japan
Phone: +81 3 5733 9541
Fax: +81 3 5733 9545

MIPS Technologies B.V.
Halver, Germany
Phone: +49 170 6365 370
Fax: +49 2353 666 920

MIPS Technologies B.V.
Nesher 36841, Israel
Derech Bar Yehuda 53 - POB 12034
Phone: +972 (545) 441 579
Fax: +972 (153) 545 441579

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